

Appl. No. 10/707,803
Amdt. dated August 03, 2005
Reply to Office action of May 12, 2005

Amendments to the Claims:

Listing of Claims:

- 5 Claim 1 (currently amended): A circuit comprising:
an operational amplifier comprising a positive input end, a negative input end, and an
output end;
a first input impedance coupled between the negative input end and a first input
signal;
10 a second input impedance coupled between the negative input end and a second input
signal; and
a first output impedance coupled between the negative input end and the output end
[[.]];
wherein resistances of the first and second input impedances are controlled by a
15 first and a second control signals respectively, so that the resistances of the first
and second impedances are substantially different from each other.

20 Claim 2 (original): The circuit of claim 1 wherein resistances of the first and second
input impedances are close to each other.

Claim 3 (original): The circuit of claim 2 wherein the circuit has a high input impedance
characteristic.

25 Claim 4 (original): The circuit of claim 2 wherein the first output impedance is a
resistive-impedance, the circuit has a high voltage attenuation characteristic.

Appl. No. 10/707,803
Amdt. dated August 03, 2005
Reply to Office action of May 12, 2005

Claim 5 (original): The circuit of claim 2 wherein the first output impedance is a capacitive-impedance, the circuit has a large time constant characteristic.

5 Claim 6 (original): The circuit of claim 1 wherein the first input impedance is a switched capacitor circuit, the switched capacitor circuit comprises:
a capacitor coupled between a first node and a ground end;
a first switch with one end coupled to the first node and another end used as an end
of the switched capacitor circuit; and
10 a second switch with one end coupled to the first node and another end used as another end of the switched capacitor circuit,
wherein the first switch and the second switch are turned on alternately by the first control signal.

Claim 7 (currently amended): A circuit comprising:
15 a differential amplifier comprising a positive input end, a negative input end, a positive output end, and a negative output end;
a first input impedance coupled between the negative input end and a first input signal;
a second input impedance coupled between the positive input end and the first input
20 signal;
a third input impedance coupled between the negative input end and a second input signal, the third input impedance being substantially equivalent to the second input impedance; and
a fourth input impedance coupled between the positive input end and the second input
25 signal, the fourth input impedance being substantially equivalent to the first input impedance;
wherein resistances of the first and second input impedances are controlled by a first and a second control signals respectively.

Appl. No. 10/707,803
Amdt. dated August 03, 2005
Reply to Office action of May 12, 2005

Claim 8 (original): The circuit of claim 7 wherein resistances of the first and second input impedances are close to each other.

- 5 Claim 9 (original): The circuit of claim 8 wherein the circuit has a high input impedance characteristic.

Claim 10 (original): The circuit of claim 7 further comprising:
10 a first output impedance coupled between the negative input end and the positive output end; and
a second output impedance coupled between the positive input end and the negative output end.

15 Claim 11 (original): The circuit of claim 10 wherein the first and the second output impedances are a resistive-impedance, the circuit has a high voltage attenuation characteristic.

20 Claim 12 (original): The circuit of claim 10 wherein at least one the first and the second output impedance is a capacitive-impedance, the circuit has a large time constant characteristic.

Claim 13 (original): The circuit of claim 7 wherein the first input impedance is a switched capacitor circuit, the switched capacitor circuit comprises:
a capacitor coupled between a first node and a ground end;
25 a first switch with one end coupled to the first node and another end used as an end of the switched capacitor circuit; and
a second switch with one end coupled to the first node and another end used as another end of the switched capacitor circuit,

Appl. No. 10/707,803
Amdt. dated August 03, 2005
Reply to Office action of May 12, 2005

wherein the first switch and the second switch are turned on alternately by the first control signal.

Claim 14 (original): A circuit comprising:

- 5 a differential amplifier comprising a positive input end, a negative input end, a positive output end, and a negative output end;
a first input impedance coupled between the negative input end and a first input signal;
a second input impedance coupled between the positive input end and the second
10 input signal;
a first output impedance coupled between the negative input end and the positive output end;
a second output impedance coupled between the negative input end and the negative output end;
15 a third output impedance coupled between the positive input end and the positive output end, the third output impedance being substantially equivalent to the second output impedance; and
a fourth output impedance coupled between the positive input end and the negative output end, the fourth output impedance being substantially equivalent to the first
20 output impedance,
wherein resistances of the first and second output impedances are controlled by a first and a second control signals respectively.

- Claim 15 (original): The circuit of claim 14 wherein resistances of the first and the second
25 output impedance are close to each other.

Claim 16 (original): The circuit of claim 15 wherein the first and the second input impedance is a resistive-impedance, the circuit has a high voltage gain characteristic.

Appl. No. 10/707,803
Amdt. dated August 03, 2005
Reply to Office action of May 12, 2005

Claim 17 (original): The circuit of claim 15 wherein at least one the first and the second input impedance is a capacitive-impedance, the circuit has a large time constant characteristic.

5

Claim 18 (original): The circuit of claim 14 wherein the first output impedance is a switched capacitor circuit, the switched capacitor circuit comprises:
a capacitor coupled between a first node and a ground end;
a first switch with one end coupled to the first node and another end used as an end of
the switched capacitor circuit; and
a second switch with one end coupled to the first node and another end used as
another end of the switched capacitor circuit,
wherein the first switch and the second switch are turned on alternately by the first control signal.

10

15

Claim 19 (currently amended): [[An]] A circuit comprising:

a differential amplifier comprising a positive input end, a negative input end, a positive output end, and a negative output end;
a first input impedance coupled between the negative input end and a first input signal;
a second input impedance coupled between the positive input end and the first input signal;
a third input impedance coupled between the negative input end and a second input signal, the third input impedance being substantially equivalent to the second input impedance;
a fourth input impedance coupled between the positive input end and the second input signal, the fourth input impedance being substantially equivalent to the first input impedance;

20

25

Appl. No. 10/707,803
Amdt. dated August 03, 2005
Reply to Office action of May 12, 2005

- a first output impedance coupled between the negative input end and the positive output end;
- a second output impedance coupled between the negative input end and the negative output end;
- 5 a third output impedance coupled between the positive input end and the positive output end, the third output impedance being substantially equivalent to the second output impedance; and
- a fourth output impedance coupled between the positive input end and the negative output end, the fourth output impedance being substantially equivalent to the first output impedance,
- 10 wherein the positive output end is for outputting a first output signal, and the negative output end is for outputting a second output signal.

Claim 20 (currently amended): The amplifying circuit of claim 19 wherein the first input impedance, the second input impedance, the third input impedance, the fourth input impedance, the first output impedance, the second output impedance, the third output impedance, or the fourth output impedance is a switched capacitor circuit.

15

Claim 21 (new): The circuit of claim 7 wherein the resistances of the first and second input impedances are so controlled by the first and the second control signals respectively, that they are substantially different from each other.

20